

### Errata for Z8420/Z84C20 NMOS/CMOS Z80 PIO Parallel Input/Output Devices

This Errata provides information on the 44-lead QFP which has been replaced by the 44-lead LQFP.

#### Affected Documents

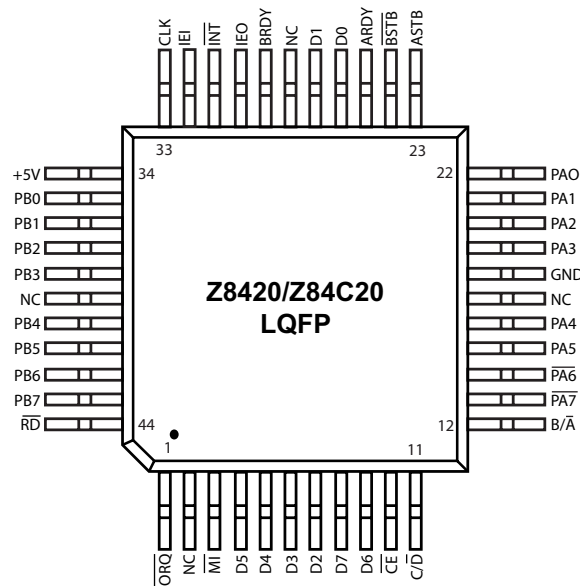
Table 1 provides the list of documents affected by the replacement of 44-lead QFP with 44-lead LQFP.

**Table 1. Documents Affected by the Replacement of 44-Lead QFP with 44-Lead LQFP**

Devices	Title	Document Number
Z8420/Z84C20	Z8420/Z84C20 NMOS/CMOS Z80 PIO Parallel Input/Output Product Specification	PS0180

#### 44-Lead LQFP

The Z8420/Z84C20 NMOS/CMOS Z80 PIO Parallel Input/Output Devices are now available in 44-lead LQFP. Figure 1 displays the pin diagram of the 44-lead LQFP and Table 2 provides the pin description.



**Figure 1. 44-Lead Z8420/Z84C20 LQFP**

**Table 2. 44-Lead Z8420/Z84C20 LQFP Description**

Pin No	Symbol	Function	Direction
1	$\overline{\text{IORQ}}$	Input/Output Request	Input
2	NC	Not Connected	N/A
3	$\overline{\text{M1}}$	Machine Cycle 1	Input
4	D5	Data 5	Input/Output
5–6	D4–D3	Data 4, 3	Input/Output
7–9	D8–D6	Data 8, 7, 6	Input/Output
10	$\overline{\text{CE}}$	Chip Enable	Input
11	$\text{C}/\overline{\text{D}}$	Control/Data Select	Input
12	$\text{B}/\overline{\text{A}}$	Port B/A Select	Input
13–16	PA7–PA4	Port A, Pins 4, 5, 6, 7	Input/Output
17	NC	Not Connected	N/A
18	GND	Ground	Input
19–22	PA3–PA0	Port A, Pins 0, 1, 2, 3	Input/Output
23	ASTB	Port A Strobe	Input
24	$\overline{\text{BSTB}}$	Port B Strobe	Input
25	ARDY	Register A Ready	Output
26–27	D0–D1	Data 0, 1	Input/Output
28	NC	Not Connected	N/A
29	BRDY	Register B Ready	Output
30	IEO	Interrupt Enable Out	Output
31	$\overline{\text{INT}}$	Interrupt Request	Input
32	IEI	Interrupt Enable In	Input
33	CLK	Clock	Input
34	+5V	Power Supply	Input
35–38	PB0–PB3	Port B, Pins 0, 1, 2, 3	Input/Output
39	NC	Not Connected	N/A
40–43	PB4–PB7	Port B, Pins 4, 5, 6, 7	Input/Output
44	$\overline{\text{RD}}$	Read	Input



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