



UP004102-0103

# Product Update

## Erratum to eZ80L92 Development Kit User Manual

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### Issue Relating to the eZ80L92 Development Kit

This document serves to highlight and/or correct an issue concerning the installation and configuration of the eZ80<sup>®</sup> Development Platform. The item below addresses an issue in the [eZ80L92 Development Kit User Manual](#) (UM0129) that is supplied with this kit.

#### Issue

An error in the Port A emulation feature of the eZ80L92 microprocessor makes reading from the port unusable. The problem was discovered in the PAL equations for the Generic Array Logic GAL221V10D device (U15) and in the schematic design of the eZ80<sup>®</sup> Development Platform. Port A emulation is accessed using memory access instructions at location 800002h, as described in the manual.

#### Workaround

ZiLOG recommends that when using the eZ80L92 Development Kit, do not include instructions in user code that attempt to read data from Port A.



**Caution:** Reading from Port A can damage the drivers used for Port A emulation and memory. Port A emulation can, however, be used for writing data.



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**ZiLOG Worldwide Headquarters**

532 Race Street

San Jose, CA 95126

Telephone: 408.558.8500

Fax: 408.558.8300

[www.ZiLOG.com](http://www.ZiLOG.com)

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